IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ronen Perets Boris Zemlyak Ariel Cohen

Serial No.:

09/748,029

Filed:

December 22, 2000

For:

Use of internal general purpose registers of a processor as a java virtual machine top of stack and dynamic allocation of the registers

according to stack status

Group Art Unit:

2183

Examiner:

Li, Aimee J.

Atty Docket:

1496.00044 / 00-177

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

10/31/05

Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 280 205

Respectfully submitted,

Timothy Croll

Reg. No. 36,771